

REMARKS

Claims 1-10 are presently pending and stand rejected. Reconsideration in view of the foregoing remarks is respectfully requested.

Claims 6-10 were rejected under 35 U.S.C. § 101 as being substantial duplicates of claims 1-5. Assignee has amended claims 6-10 and submits that claims 1-5 are systems claims while claims 6-10 are apparatus claims. Assignee respectfully submits that direction of the claims towards different statutory categories overcomes this rejection.

Claims 1 and 6 were rejected under 35 U.S.C. § 102 as being anticipated by Tsukagoshi. Claim 1 recites, among other limitations, “a first memory for storing a reference video”. Examiner has indicated that Tsukagoshi teaches “a first memory (Fig. 1b-100; paragraph 0028], ‘signal input’) for storing a reference video”. Assignee respectfully traverses the rejection because a ‘signal input’ is not *memory*. Note for example, that in Figure 1, “signal input” even receives analog signals. Claims 1 and 6 also recite, among other limitations, “writing the decoded reference video to a second memory”. Examiner has indicated that Tshagoshi teaches “writing the decoded reference video (Fig. 1b, ‘v2’) to the second memory (Fig. 1b-150; paragraph [0028], ‘video and audio output’)”. Assignee respectfully traverses the rejection and submits that the “video and audio output” are not *memory*. Accordingly, Assignee respectfully requests that Examiner withdraw the rejection to claims 1 and 6, as well as to dependent claims 2-5 and 7-13.

Claim 3 was rejected under 35 U.S.C. § 102 as being anticipated by Tsukagoshi. Claim 3 is amended to recite, among other limitations, “an adapter for connecting the digital input/output card to the video decoder, said adapter and capable of removing the digital input/output card from the video decoder”. Although Examiner has indicated that Tsukagoshi teaches “an adapter (Fig. 1b-135; paragraph 0029, ‘Demultiplexer’)”, Assignee respectfully submits that the foregoing is not “capable of *removing the digital input/output card from the video decoder*”. Accordingly, Assignee respectfully requests that Examiner withdraw the rejection to claim 3 and 8.

Claim 11 is added and recites, among other limitations, “the processor provides the encoded video data *directly* to the video decoder.” It is noted that in Tsukagoshi, that

the encoded video data is written to the storage or transmission medium. The “Encoded signals are retrieved from storage or transmission medium 160 and transferred to decoder system 170”. Accordingly, Assignee respectfully submits that Tsukagoshi does not teach that “the processor provides the encoded video data *directly* to the video decoder”, and requests allowance for claim 11.

Claim 12 is added and recites, among other limitations, “the processor provides the encoded video data to the video decoder immediately after encoding.” It is noted that Tsukagoshi is directed to a “System and Method for Timeshifting the Encoding/Decoding of Audio/Visual Signals in Real Time”. Title. “After a period of time, the encoded bitstream is retrieved and decoded.”. Abstract. Accordingly, Assignee respectfully submits that Tsukagoshi does not teach the foregoing and requests allowance for claim 12.

Claim 13 is added and recites, among other limitations, “the first memory stores the entire reference video while the second memory stores the entire decoded video.” Even if, for the sake of argument, the Signal Input 110 and Video & Audio Output are deemed to be memory, Tsukagoshi does not teach that they store the reference video and the decoded video at the same time. Accordingly, allowance is respectfully requested for claim 13.

CONCLUSION

For at least the foregoing reasons, each of the pending claims are now in a condition for allowance and Examiner is requested to pass this case to issuance.

The Commissioner is hereby authorized to charge additional fees or credit overpayments to the deposit account of McAndrews, Held & Malloy, Account No. 13-0017.

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Respectfully submitted,



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